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PARAMETER	DEFINITION	MIN	MAX	UNIT

PARAMETER **DEFINITION**

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SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Reference and Control Loop						
Current Limit and Over Current Protection						
Enable and Soft Startup						
Switching Frequency and External Clock Synchronization						

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Overview

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Typical Application

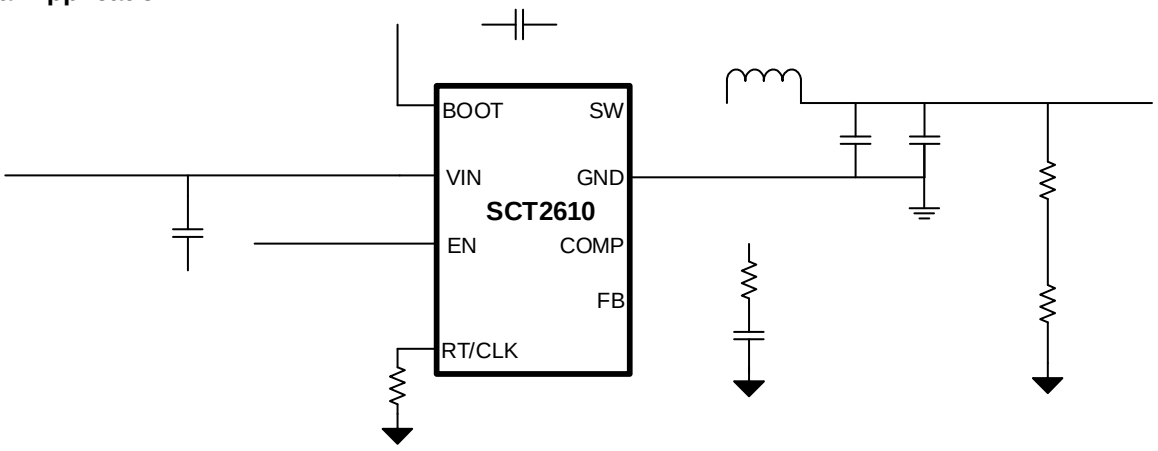

Figure 11. SCT2610 Design Example, 3.3V Output with Programmable UVLO

Design Parameters

Design Parameters	Example Value

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Output Voltage

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Inductor Selection

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Diode Selection

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Table 3: Compensation Values for Typical Output Voltage/Capacitor Combinations at fsw=500KHz

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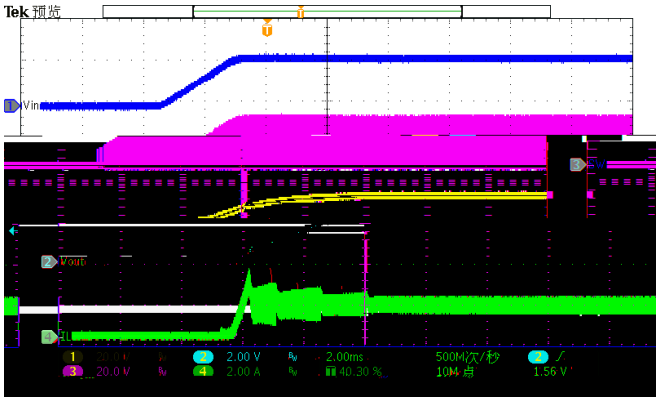
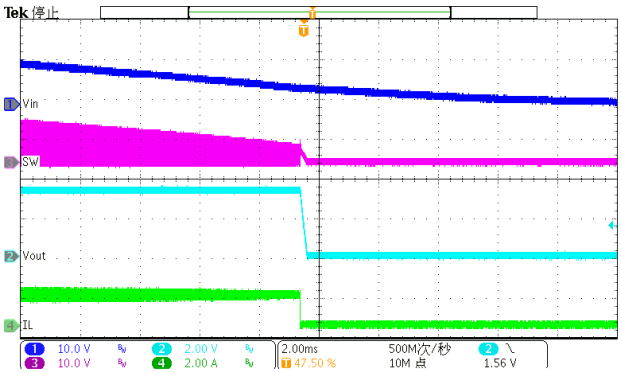
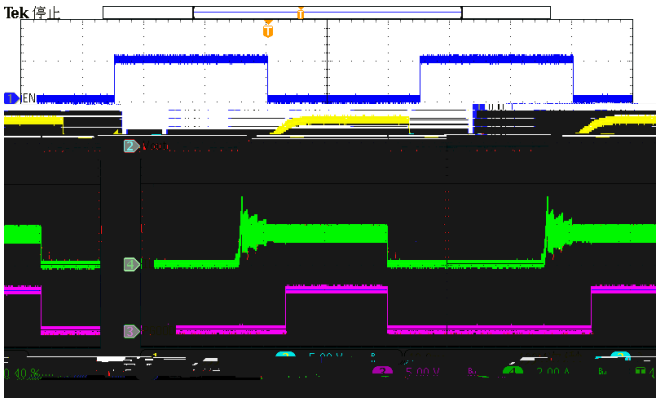
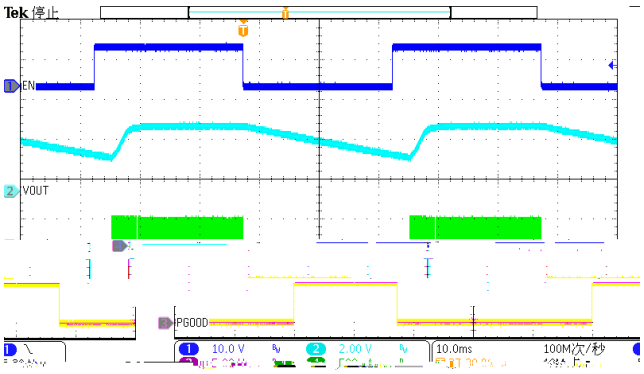
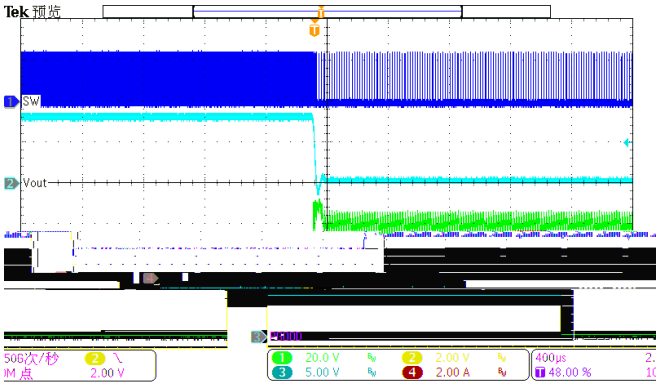
Application Waveforms

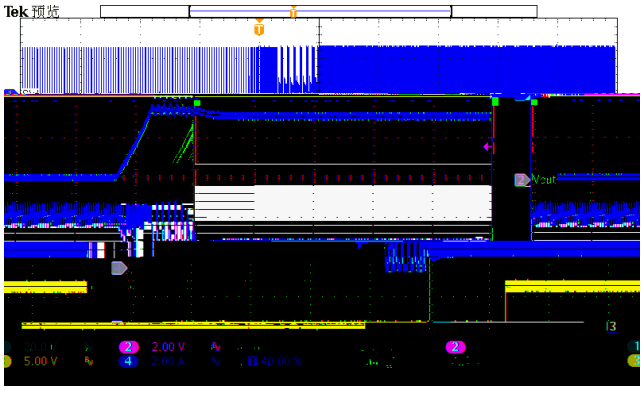












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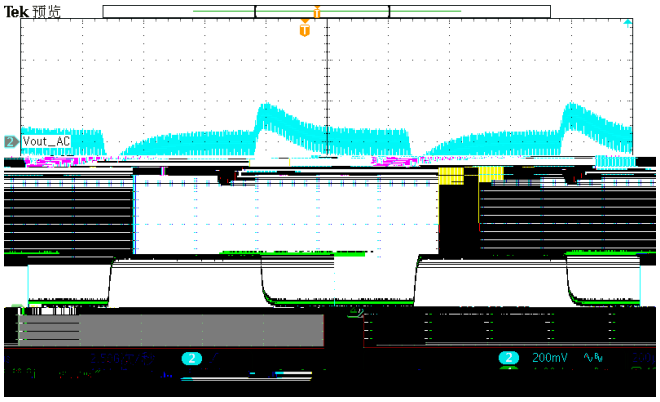
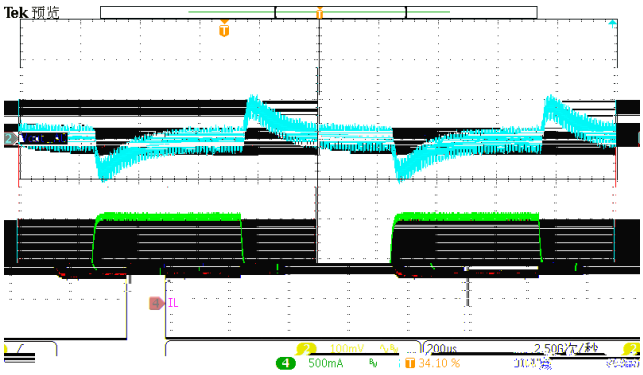
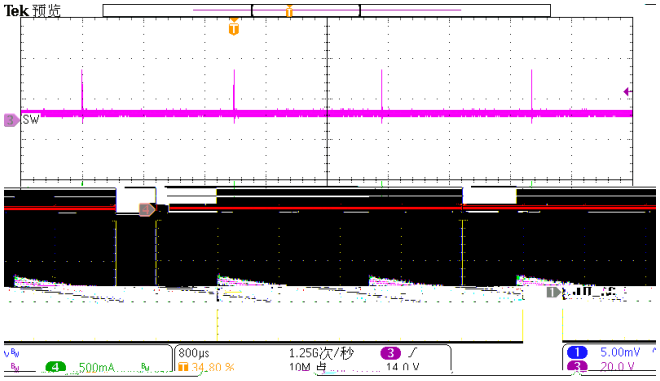
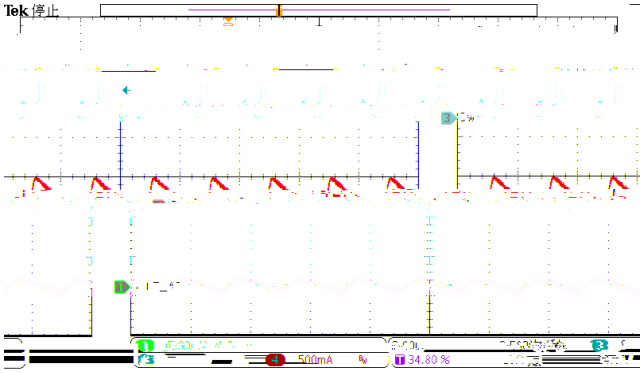
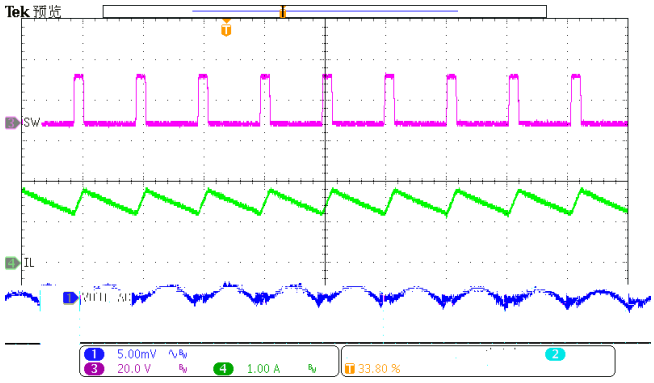
Application Waveforms(continued)

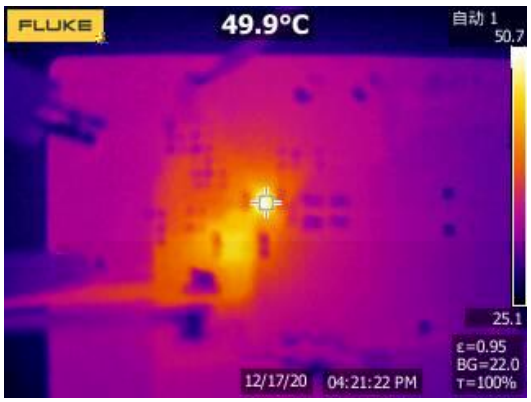












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